

FIG. 1

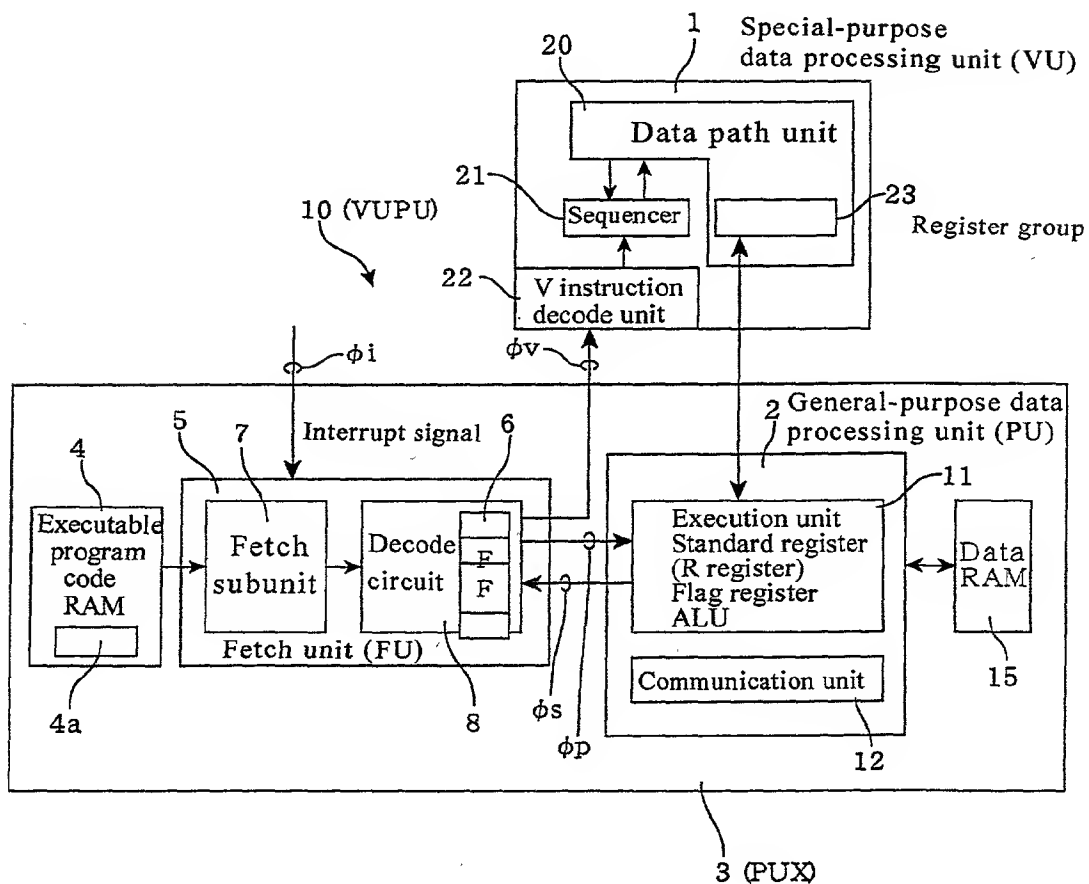


FIG. 2

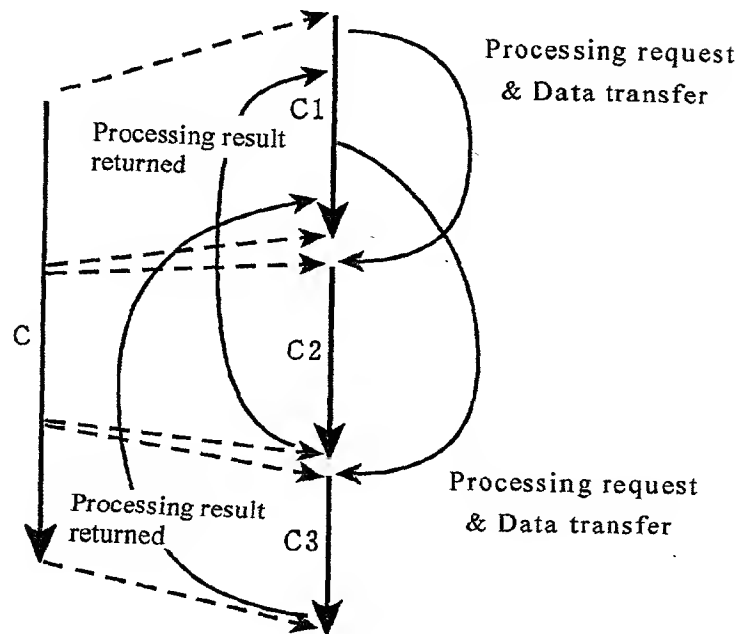


FIG. 3

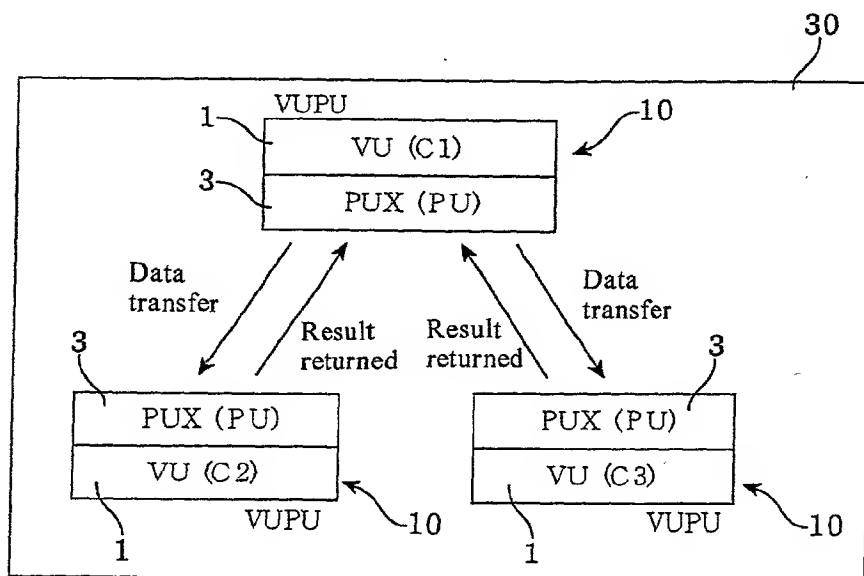


FIG. 4

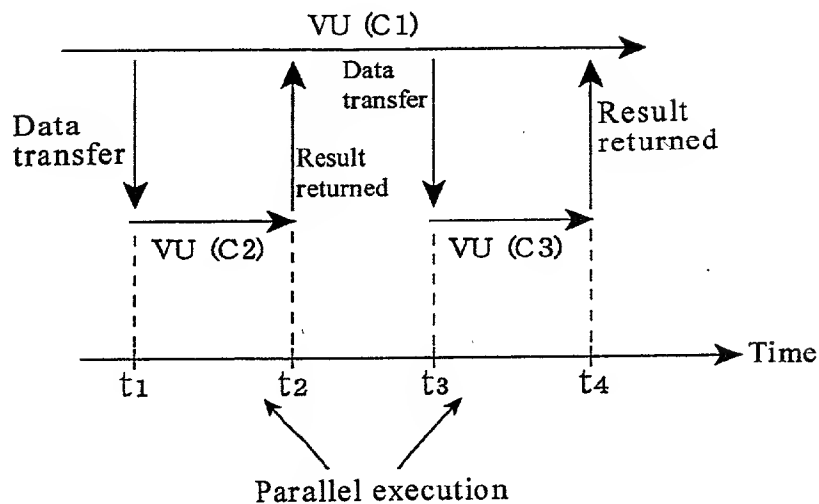


FIG. 5

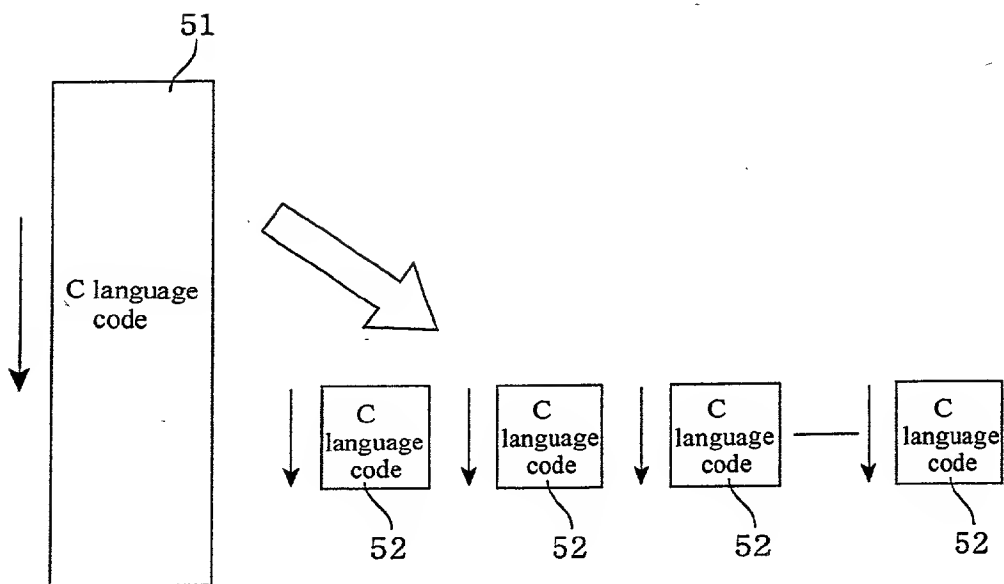


FIG. 6

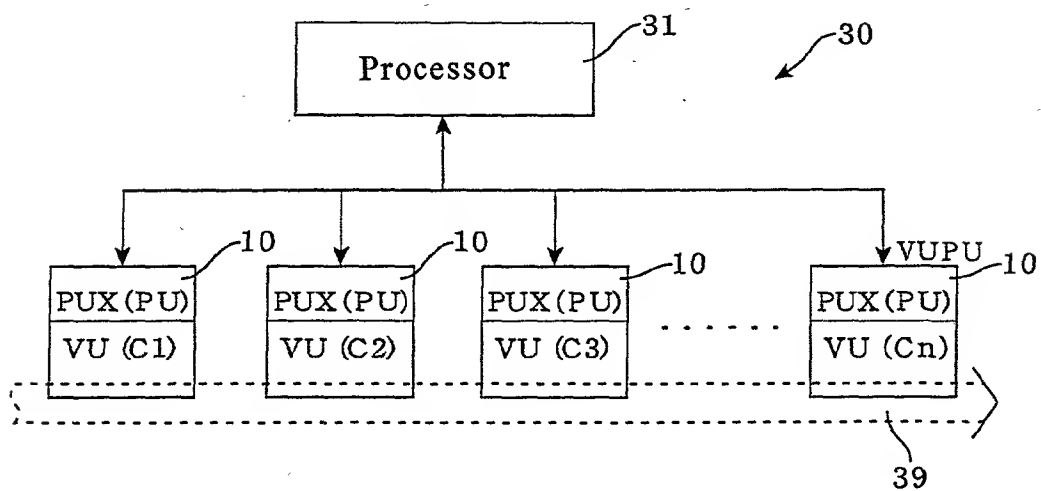


FIG. 7

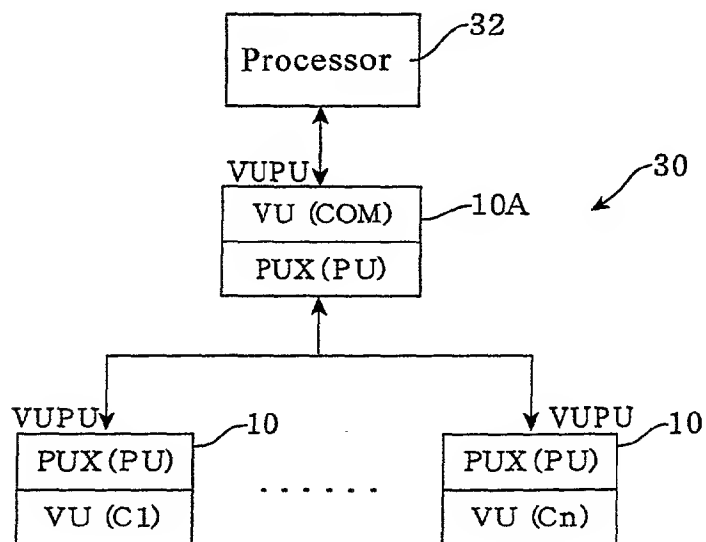


FIG. 8

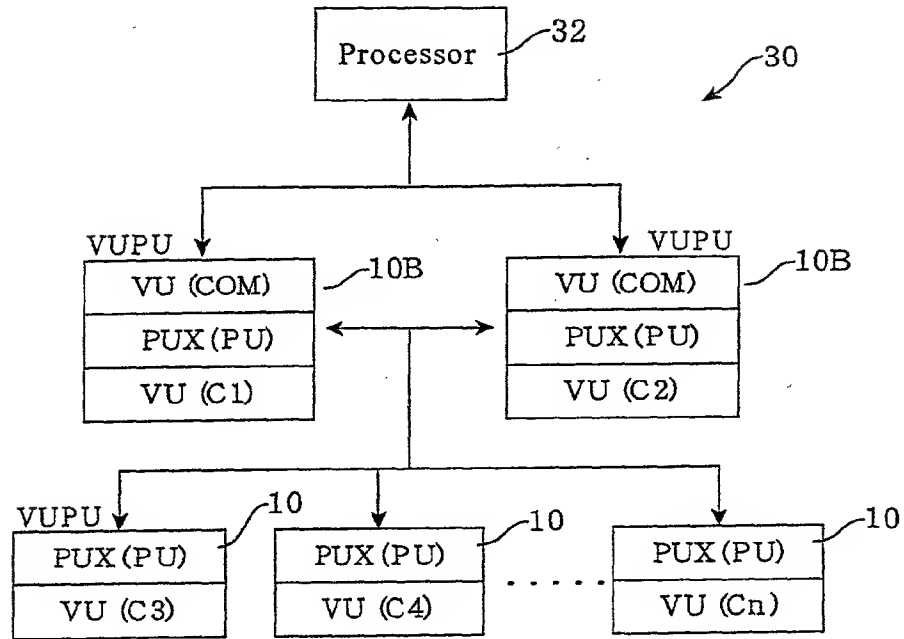


FIG. 9

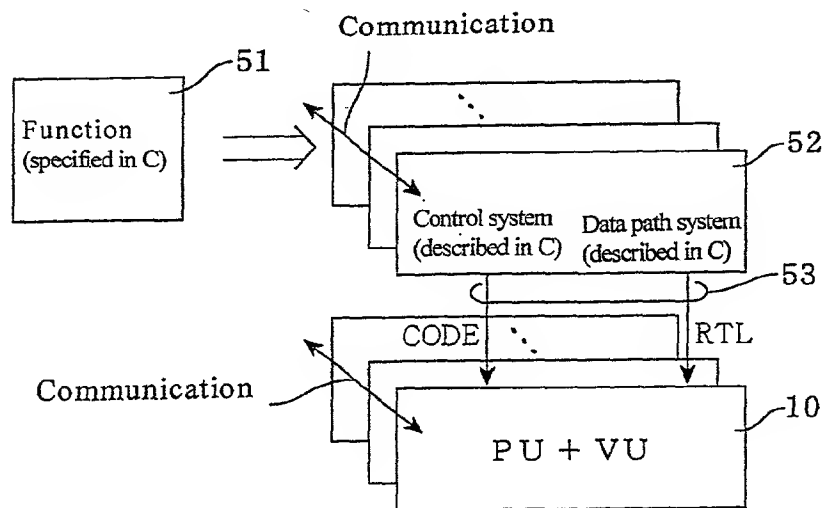


FIG. 10

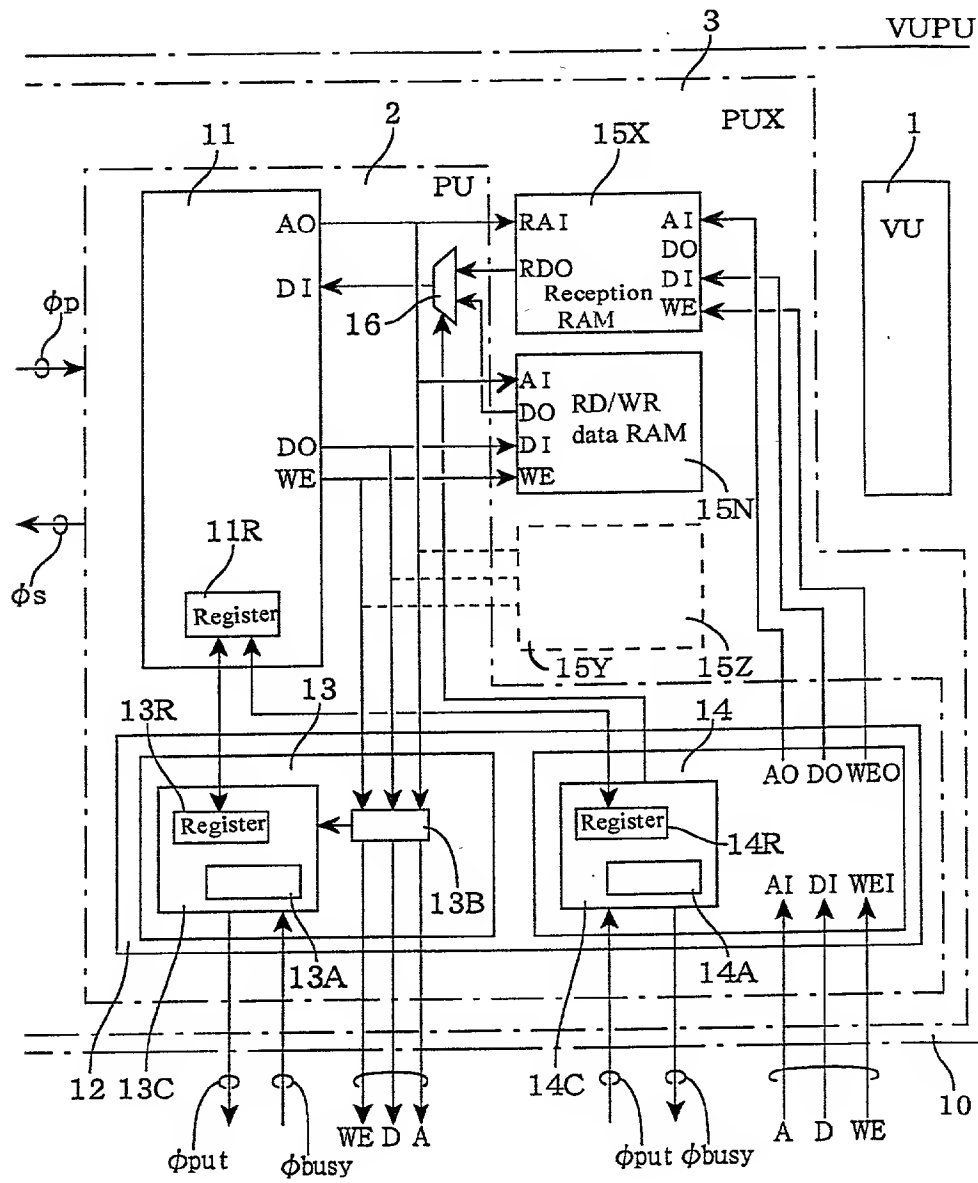


FIG. 11

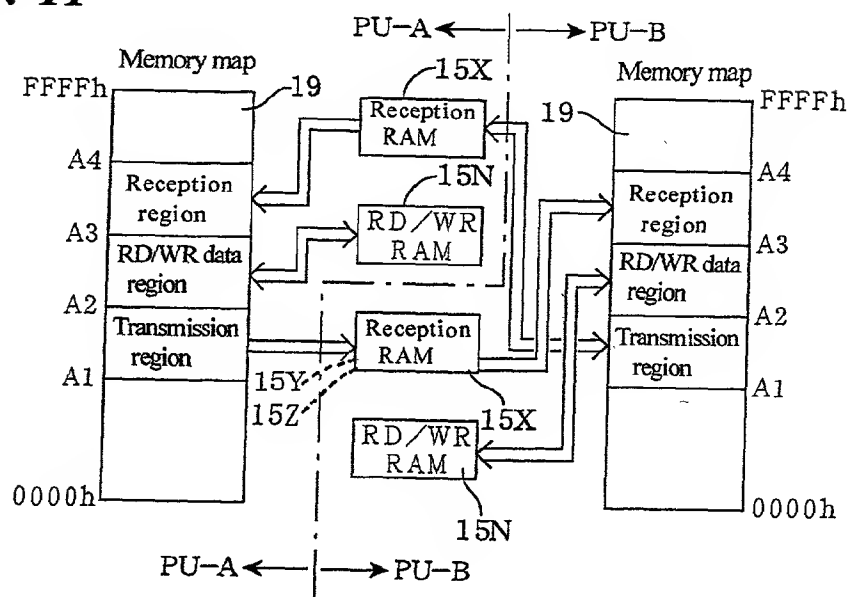


FIG. 14

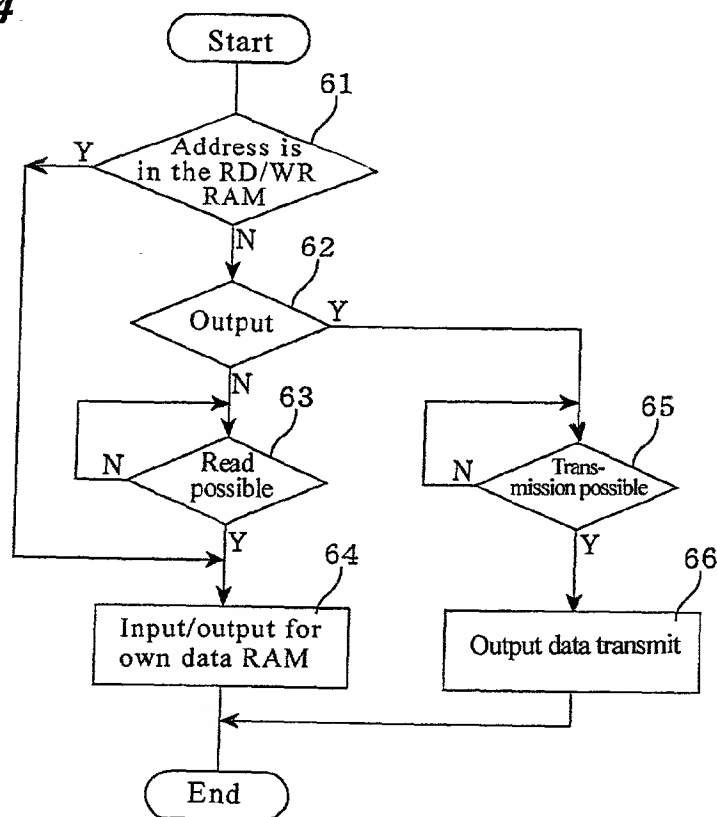


FIG. 12

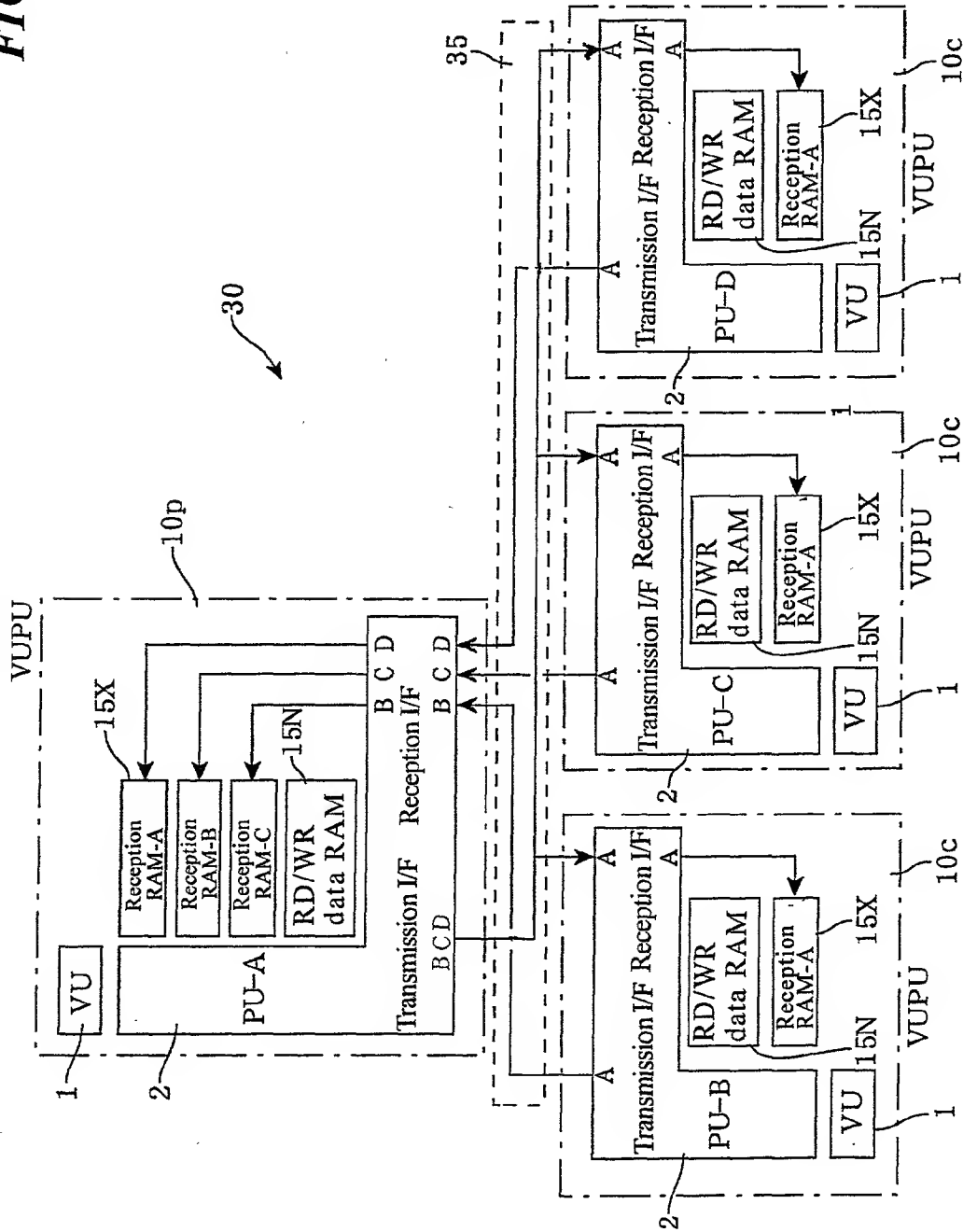


FIG. 13

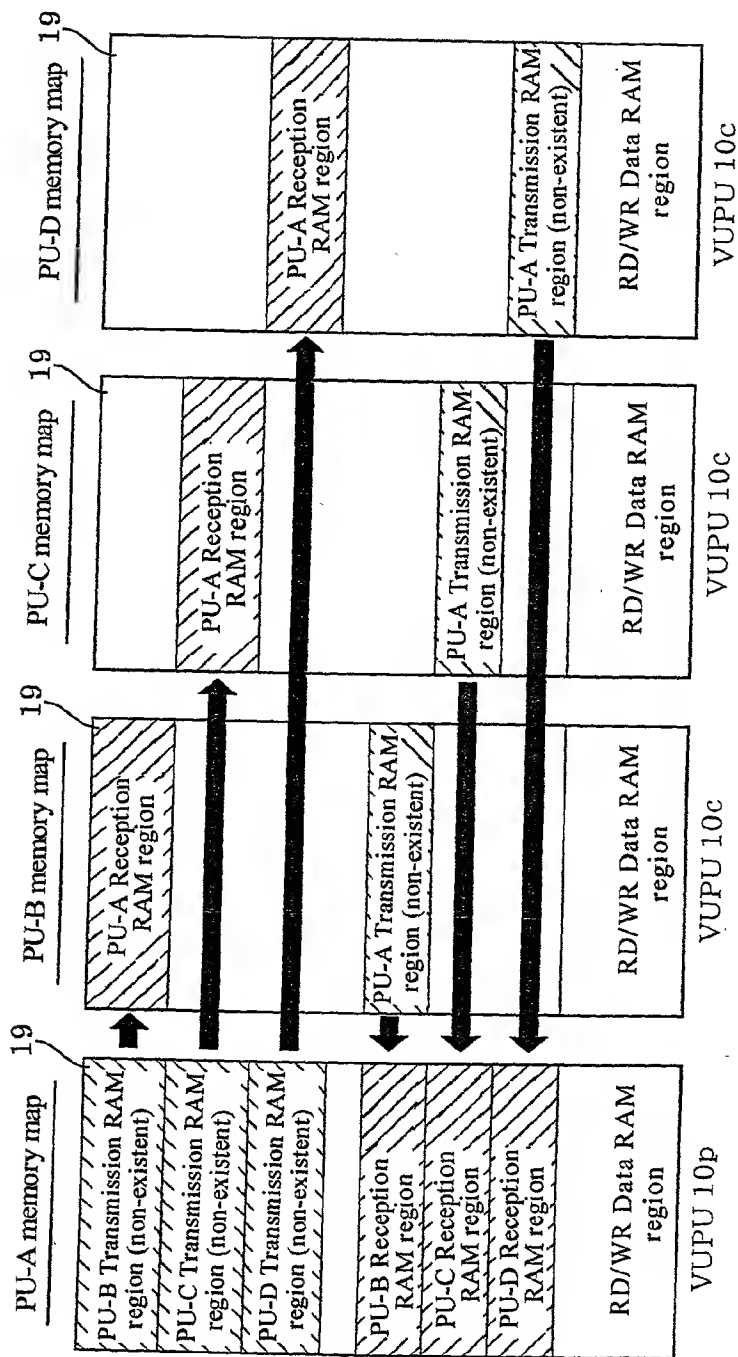


FIG. 15

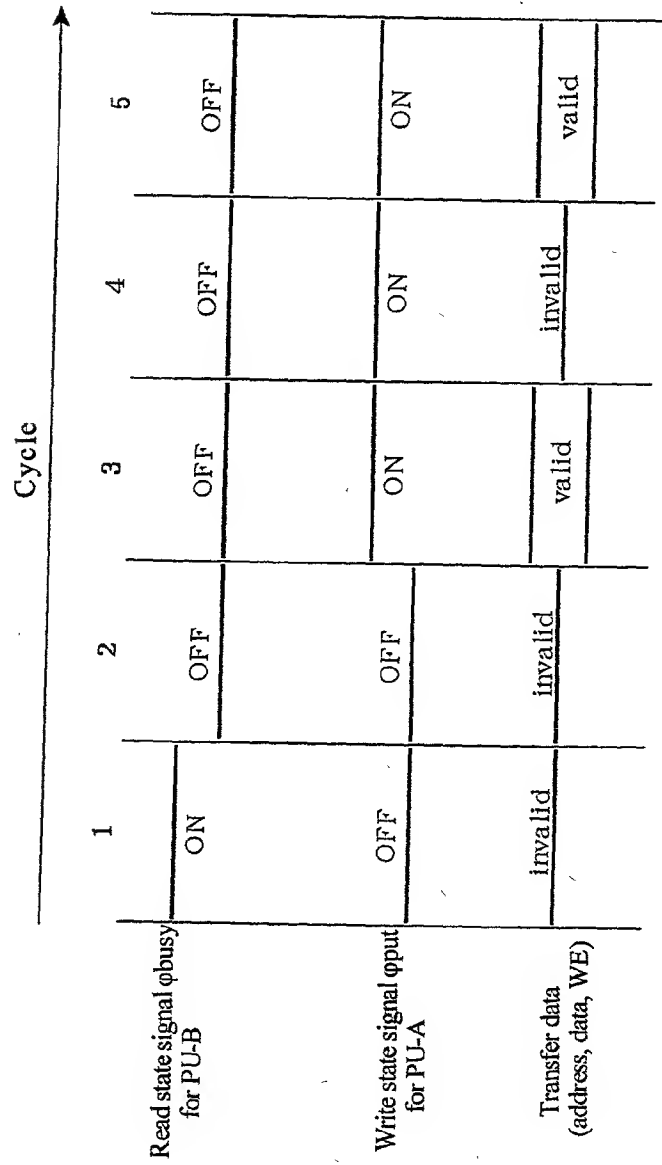


FIG. 16A

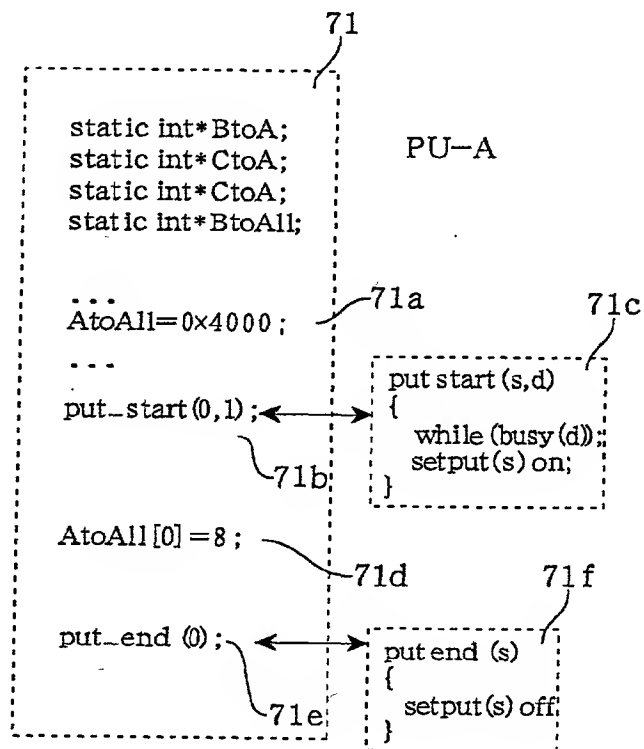


FIG. 16B

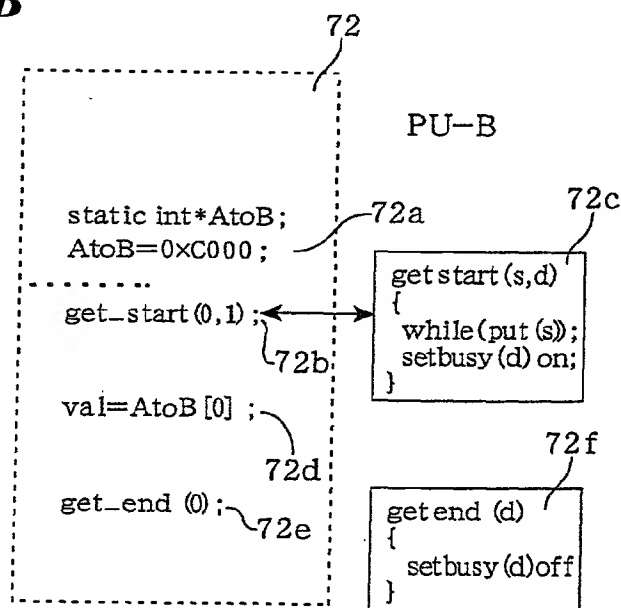


FIG. 17A

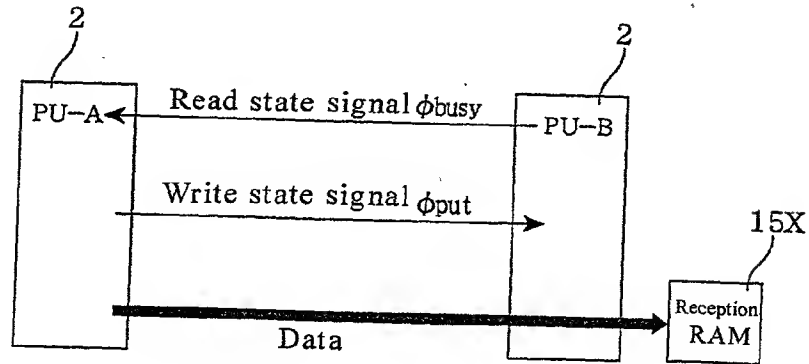


FIG. 17B

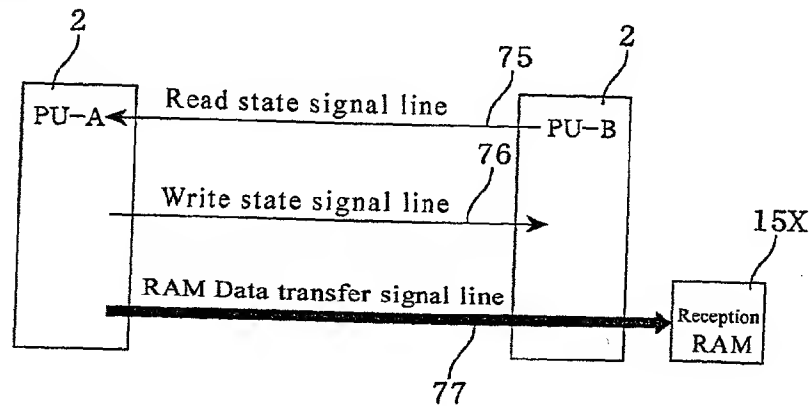


FIG. 18A

71
PU-A

```
static int*BtoA;  
static int*CtoA;  
static int*DtoA;  
static int*AtoAll;  
  
...  
BtoA=0x0000 71g  
AtoAll=0x4000; 71a  
  
...  
while (BtoA (0)=1); 71h  
AtoAll (0) =1; 71i  
AtoAll (1) =1; 71j  
AtoAll (0) =0; 71k
```

FIG. 18B

72
PU-B

```
static int*AtoB; 72a  
AtoB=0xC000; 72a  
BtoA=0x0000 72g  
  
while (AtoB (0)=1); 72h  
BtoA (0) =1; 72i  
val=AtoB (1) ; 72j  
BtoA (0) =0; 72k
```

FIG. 19A

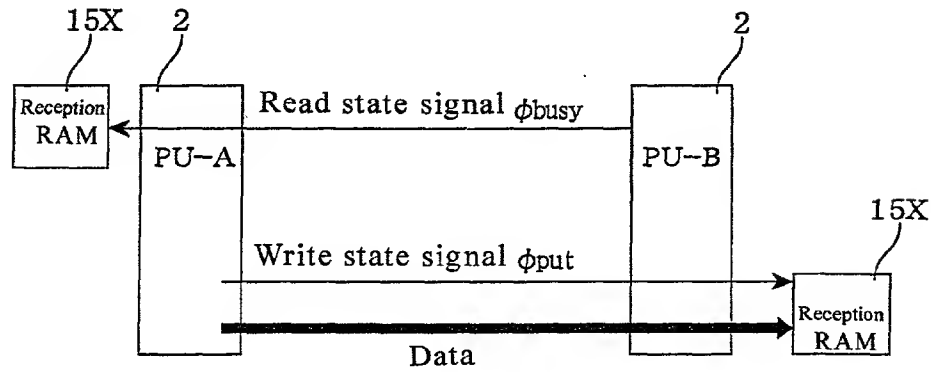


FIG. 19B

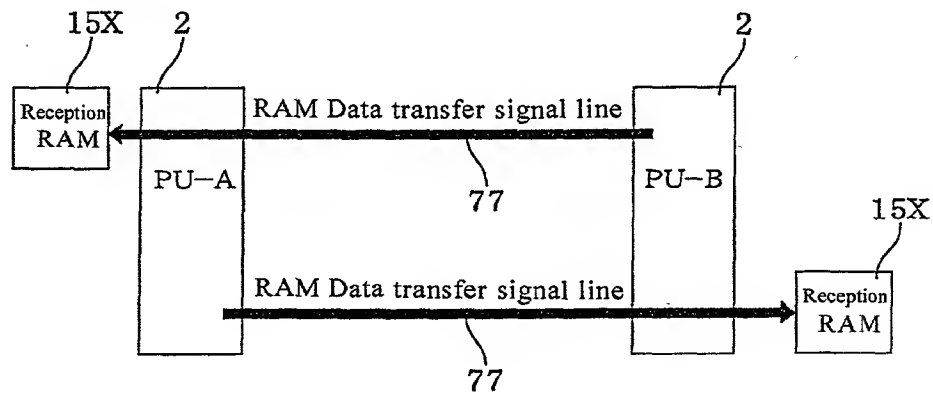


FIG. 20

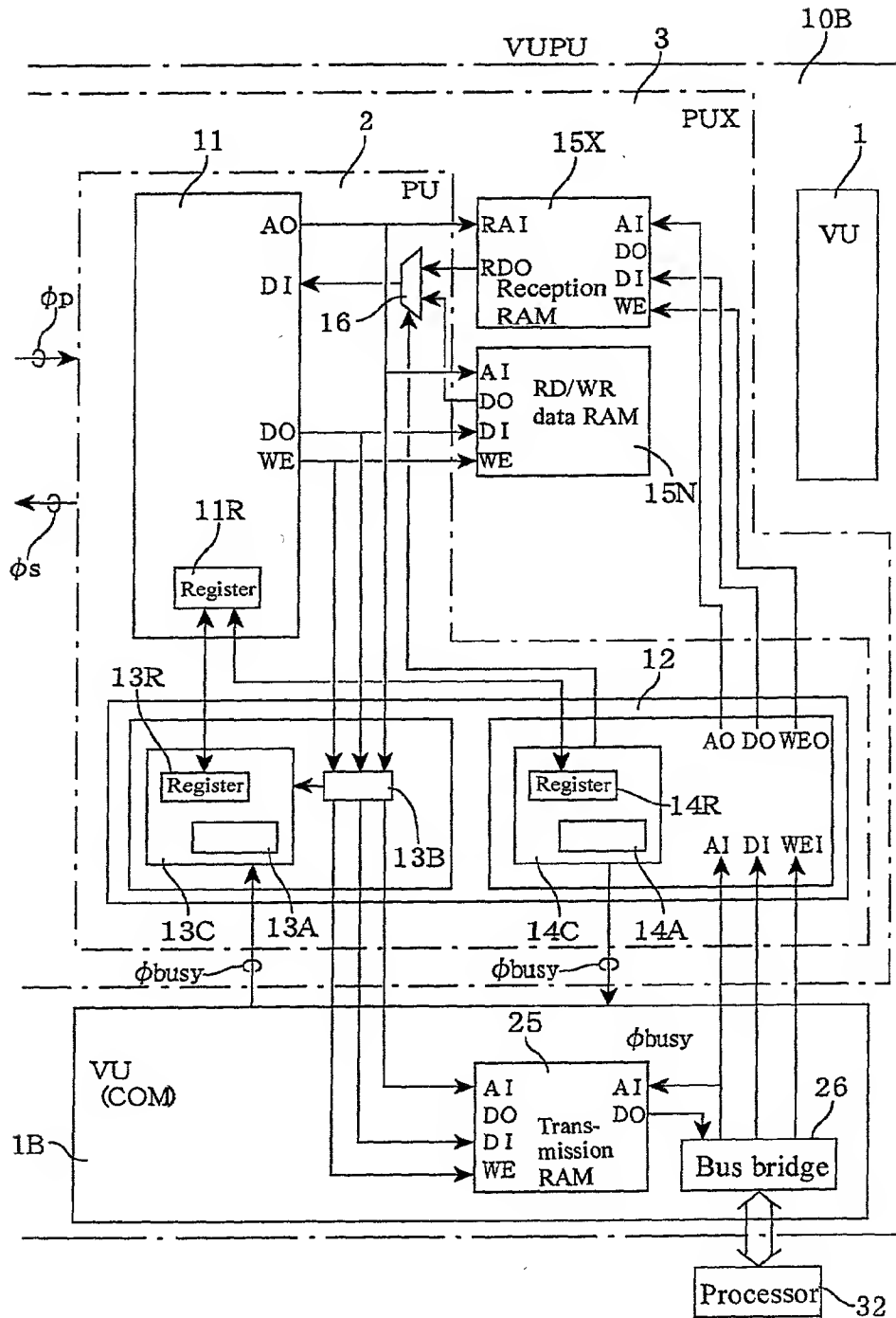


FIG. 21

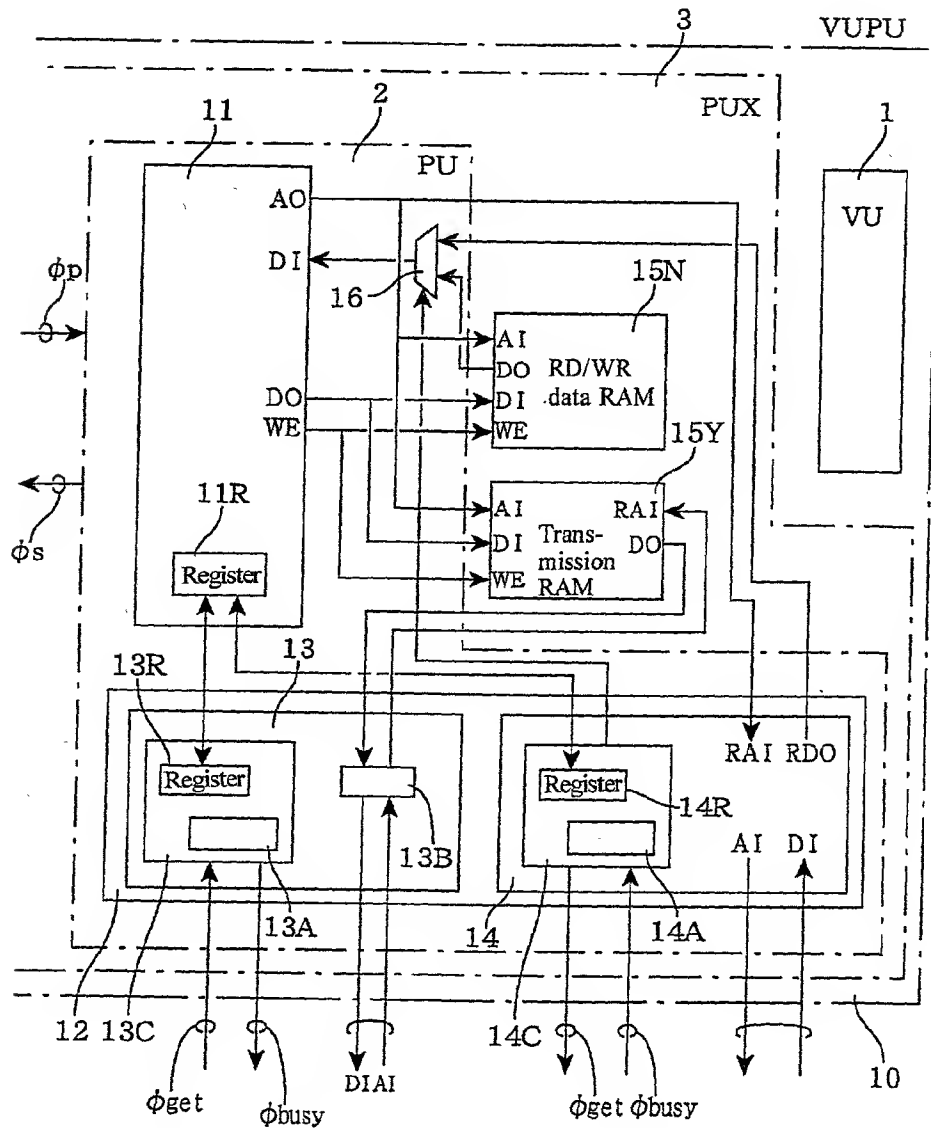
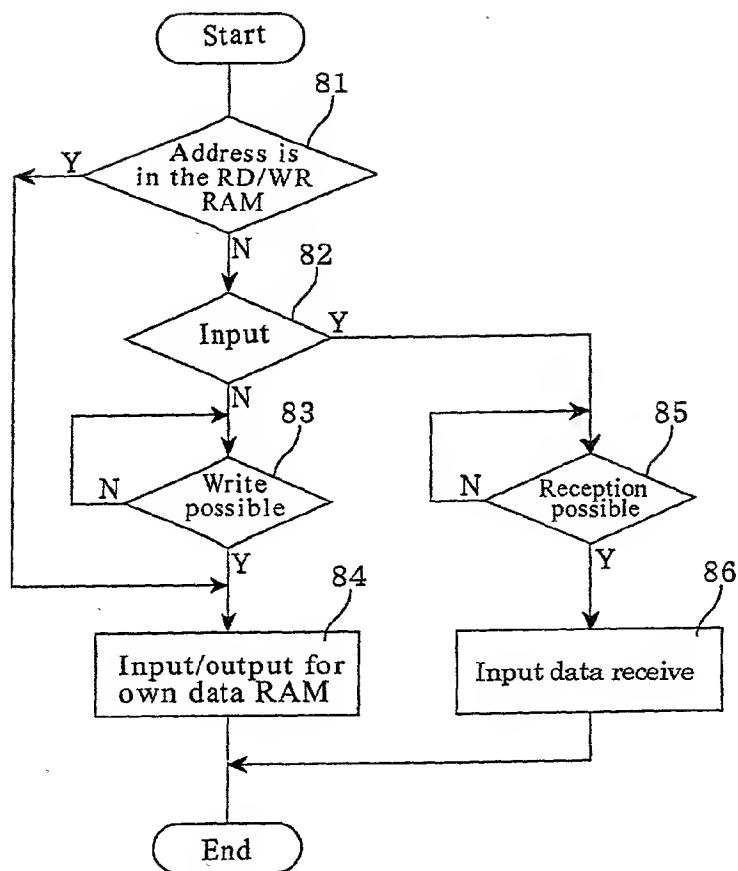


FIG. 22



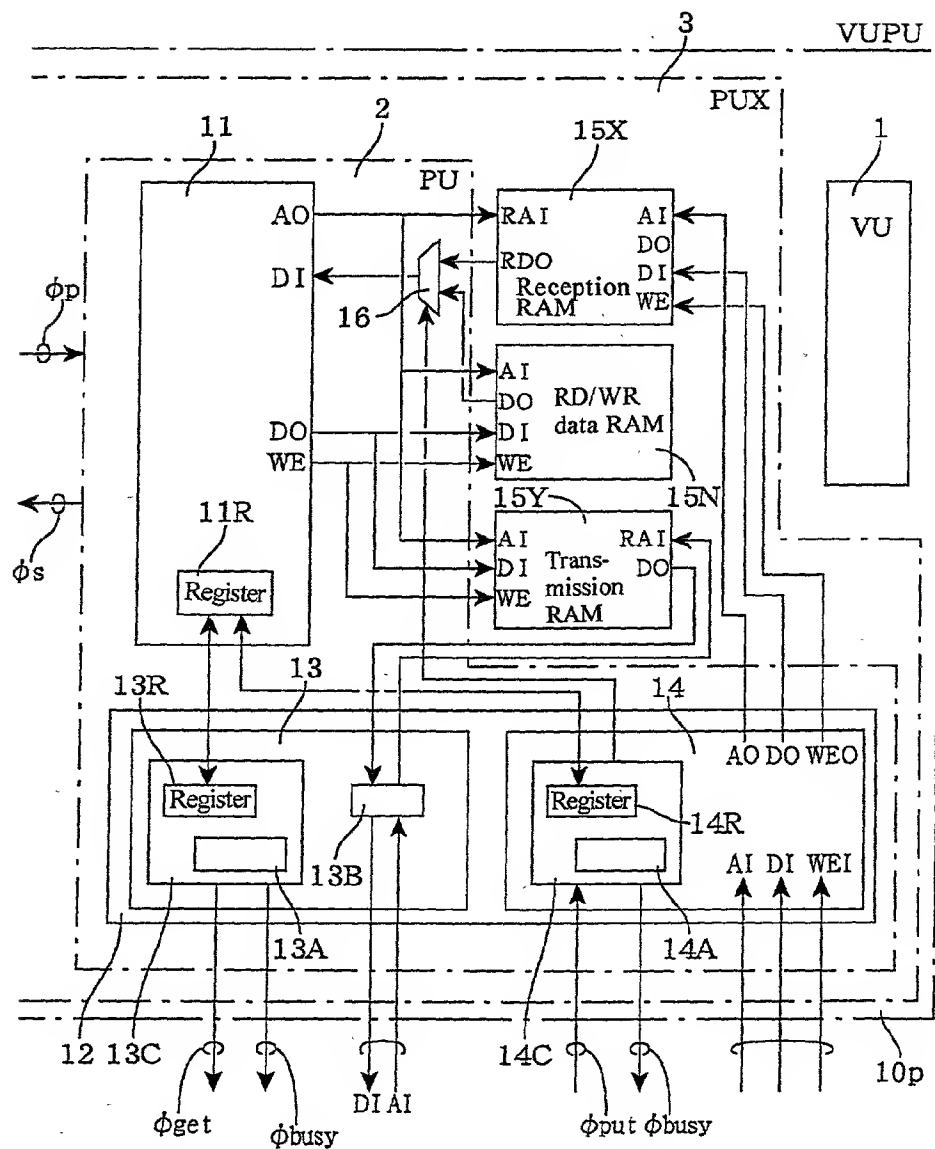


FIG. 24

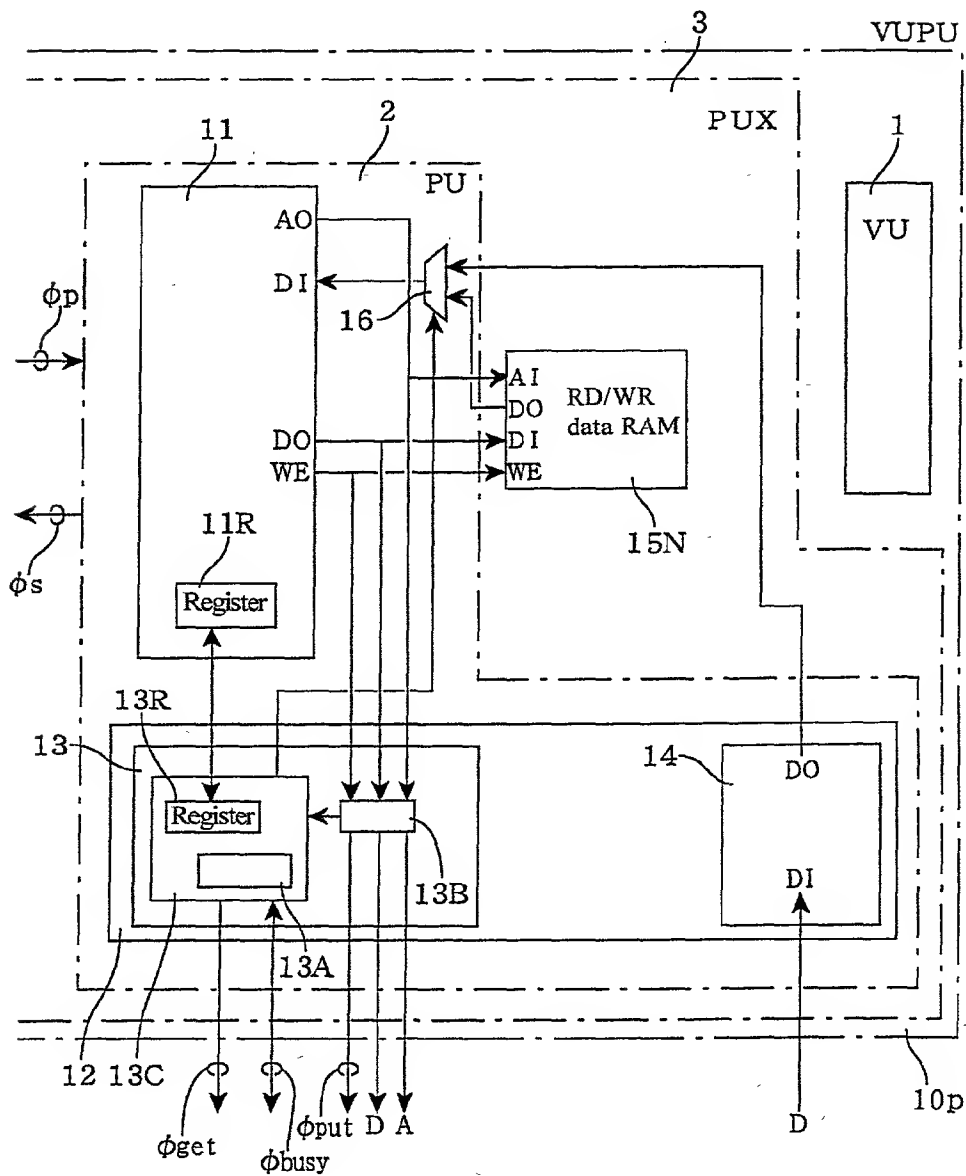


FIG. 25

